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| Sl.No | Memory Mapped I/O | Isolated I/O |
| 1 | Processor and I/O device share the same memory location | Processor and I/O device have different memory locations |
| 2 | I/O devices are mapped through memory address. The address of the port will be stored in memory locations | I/O devices are mapped through port addresses |
| 3 | Single set of read and write lines are used | Different set of read and write lines are used |
| 4 | All instructions related to memory can be used with I/O devices | Distinct set of instructions are used for I/O and memory |
| `5 | Single address bus to address both memory and I/O | Requires two different buses |

**Difference between memory mapped and Isolated I/O:**